

## 1 Introduction

This document illustrates and describes critical signal routing through the CMX7241 and CMX7341 PMR Common Platform Processors when the dPMR<sup>®</sup> + Analogue Function Image<sup>™</sup> is loaded. References to related C-BUS write registers are also indicated to provide easier understanding of the device and its control.

This guide applies to operation with the Function Image<sup>™</sup> 7241/7341FI-1.1.5.3 loaded. It will be updated, as required, should additional features or changes be made to this Function Image<sup>™</sup>.

The 7241/7341FI-1.x Configuration Guide is not intended to comprehensively describe all of the hardware in a typical radio nor all of the functions or possible set-ups. This document is a virtual representation of the device and is provided for illustrative purposes to simplify and aid product development.

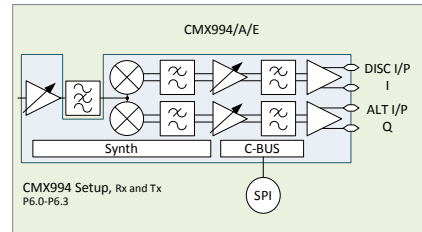
Default settings are shown in red and the Configuration Guide is best printed on an A3 colour printer.

## 2 History

Version	Changes	Date
1.0	First Issue	17-3-17

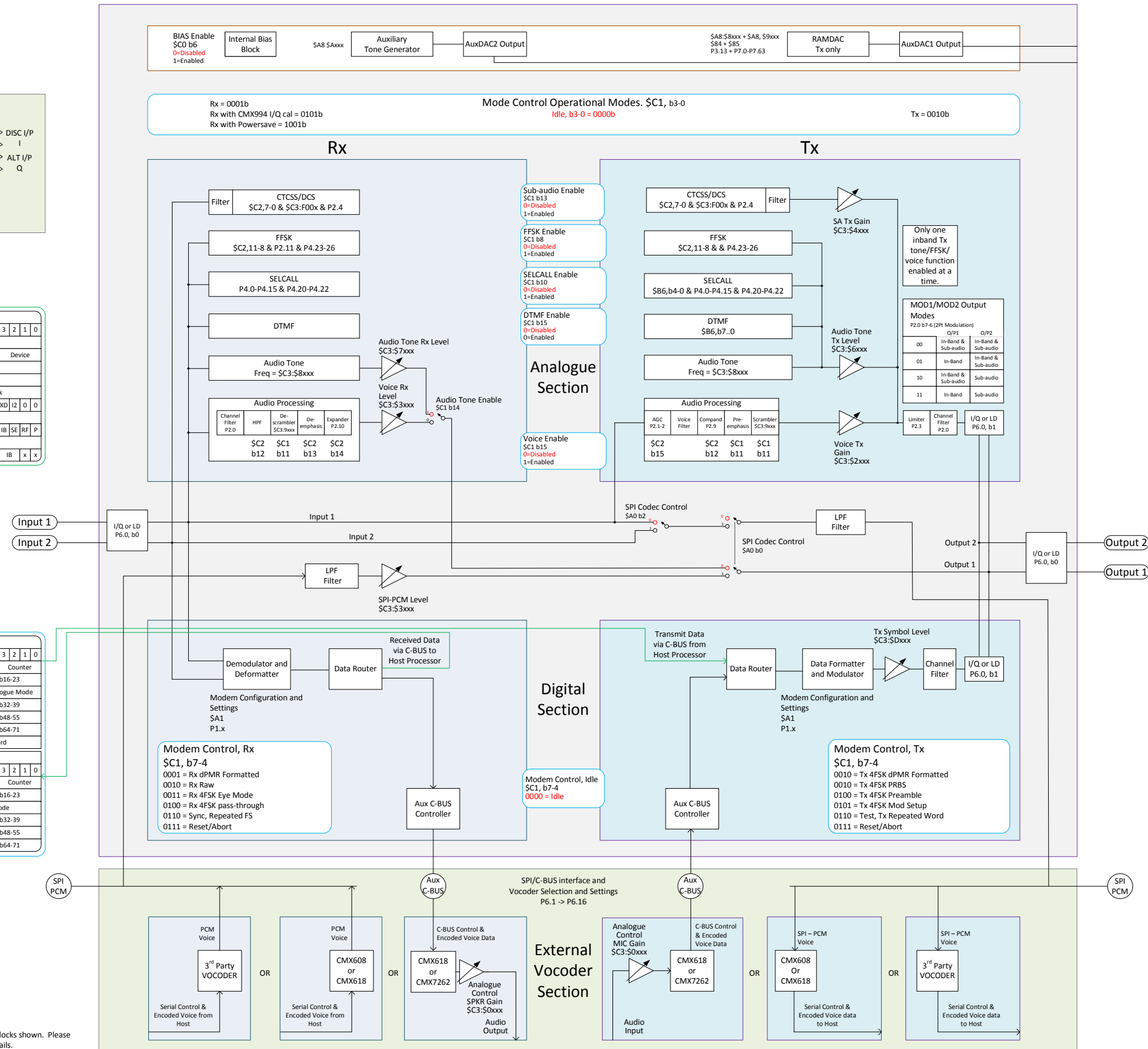
# CMX7241/CMX7341 PMR Common Platform Processor

## 7241/7341FI-1.x dPMR and Analogue PMR with I/Q support, Core Functions



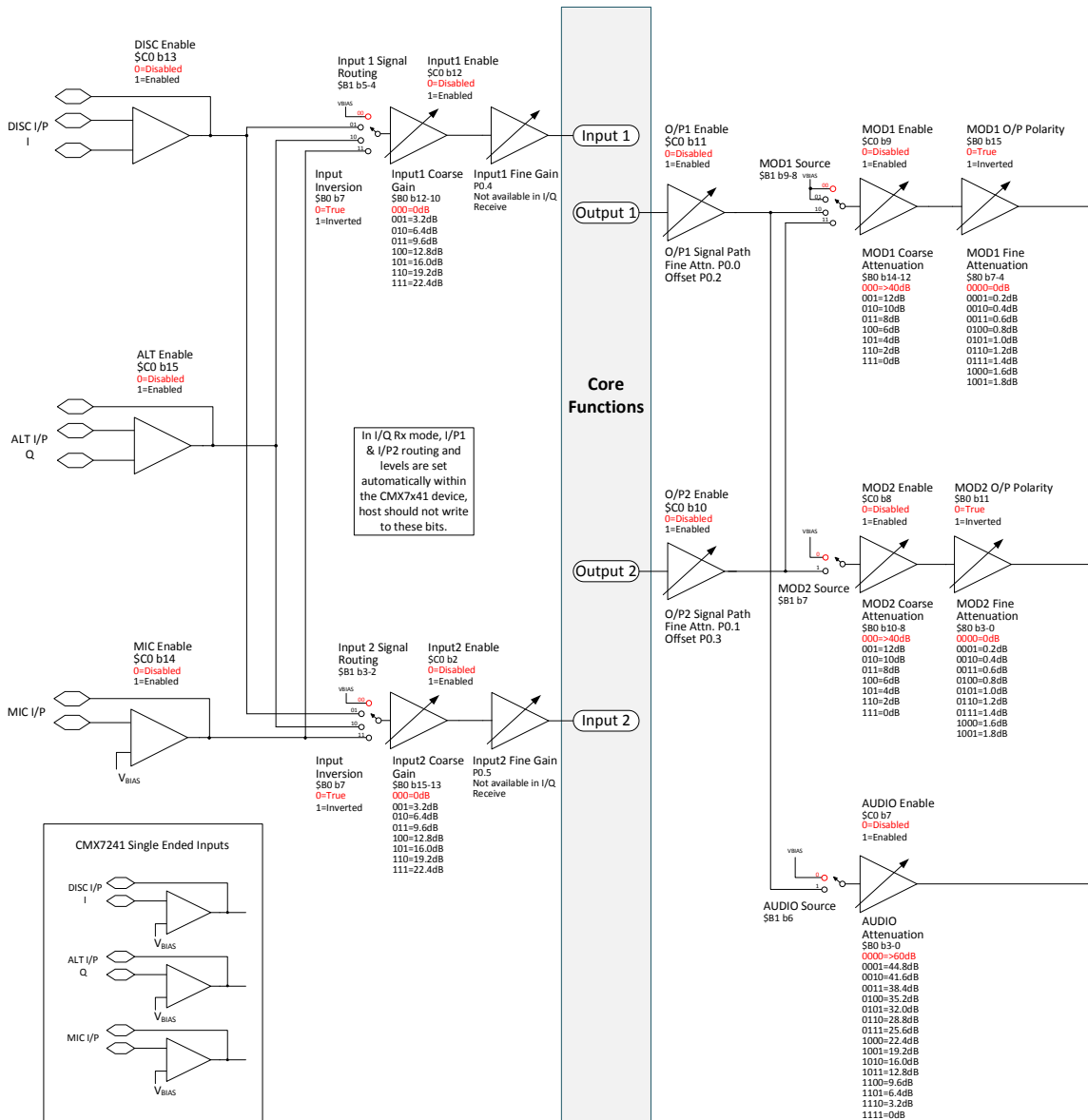
Register	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Mode Control Read back																
SA9																
Analogue PMR Status																
Power Down Control Read back																
SC4																
IRQ Status																
SC6																
4FSK Modem Status																
SC9																

Register	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
TxDATA b0-7																
TxDATA b8-15																
TxDATA b16-23																
TxDATA b24-31																
TxDATA b32-39																
TxDATA b40-47																
TxDATA b48-55																
TxDATA b56-63																
TxDATA b64-71																
RxDATA b0-7																
RxDATA b8-15																
RxDATA b16-23																
RxDATA b24-31																
RxDATA b32-39																
RxDATA b40-47																
RxDATA b48-55																
RxDATA b56-63																
RxDATA b64-71																



For clarity not all C-BUS registers or functional blocks shown. Please refer to the latest user manual for complete details.

# CMX7241/CMX7341 PMR Common Platform Processor Signal I/O






## About FirmASIC<sup>®</sup>

CML's proprietary FirmASIC<sup>®</sup> component technology reduces cost, time to market and development risk, with increased flexibility for the designer and end application. FirmASIC<sup>®</sup> combines Analogue, Digital, Firmware and Memory technologies in a single silicon platform that can be focused to deliver the right feature mix, performance and price for a target application family. Specific functions of a FirmASIC<sup>®</sup> device are determined by uploading its Function Image<sup>™</sup> during device initialization. New Function Images<sup>™</sup> may be later provided to supplement and enhance device functions, expanding or modifying end-product features without the need for expensive and time-consuming design changes. FirmASIC<sup>®</sup> devices provide significant time to market and commercial benefits over Custom ASIC, Structured ASIC, FPGA and DSP solutions. They may also be exclusively customised where security or intellectual property issues prevent the use of Application Specific Standard Products (ASSP's).

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